

What is claimed is:

- 5 ^{Sub} 1. A driving method for a display apparatus, wherein a clock used for driving a display panel is continuously varied in frequency, and said display panel is driven with said frequency varying clock.
2. The driving method for a display apparatus as claimed in claim 1, wherein said clock used for driving said display panel is a source clock of said display apparatus.
- 10 3. The driving method for a display apparatus as claimed in claim 1, wherein said clock used for driving said display panel continuously varies within a range of plus or minus 1 percent of a reference frequency.
- 15 ^{Sub} 4. The driving method for a display apparatus as claimed in claim 1, wherein said display apparatus is a plasma display apparatus.
- 20 5. The driving method for a display apparatus as claimed in claim 1, wherein control of said clock used for driving said display panel is performed during a quiescent period.
- 25 6. A driving method for a display apparatus, wherein at least two frequencies are provided for a clock used for driving a display panel, by sequentially switching said clock between said at least two frequencies, said display panel is driven with said switched clock.
- 30 7. The driving method for a display apparatus as claimed in claim 6, wherein two frequencies lying within plus or minus 1 percent of a reference frequency are set for said clock used for driving said display panel.
8. The driving method for a display apparatus as claimed in claim 6, wherein said display apparatus is a plasma display apparatus.
- 35 ^{Sub} 9. The driving method for a display apparatus as claimed in claim 6, wherein control of said clock used for driving said display panel is performed during a quiescent period.

10. A driving method for a display apparatus,
wherein drive waveforms for a display panel are provided
corresponding to at least two frequencies, and said
display panel is driven by sequentially switching an
5 output drive waveform between said drive waveforms
corresponding to said at least two frequencies.

11. The driving method for a display apparatus as
claimed in claim 10, wherein said drive waveforms for
said display panel are provided corresponding to two
10 frequencies lying within plus or minus 1 percent of a
reference frequency.

12. The driving method for a display apparatus as
claimed in claim 10, wherein said display apparatus is a
plasma display apparatus.

15 13. The driving method for a display apparatus as
claimed in claim 10, wherein control of said clock used
for driving said display panel is performed during a
quiescent period.

14. A display apparatus comprising a clock
20 generating circuit, a drive waveform generating circuit
for generating a drive waveform by using a clock from
said clock generating circuit, and a display panel for
displaying an image in accordance with said drive
waveform, wherein:

25 said clock generating circuit generates a
clock whose frequency varies continuously, and said drive
waveform generating circuit drives said display panel by
outputting a drive waveform whose frequency varies in
accordance with said frequency varying clock.

30 15. The display apparatus as claimed in claim 14,
wherein said clock generating circuit generates the
source clock of said display apparatus.

16. The display apparatus as claimed in claim 14,
wherein said clock generating circuit generates a clock
35 whose frequency varies continuously within a range of
plus or minus 1 percent of a reference frequency.

17. The display apparatus as claimed in claim 14,

44-38861-100

wherein said display apparatus is a plasma display apparatus.

18. The display apparatus as claimed in claim 14,
wherein during a quiescent period, said clock generating
circuit performs control of said clock used for driving
said display panel.

19. A display apparatus comprising a clock
generating circuit, a drive waveform generating circuit
for generating a drive waveform by using a clock from
said clock generating circuit, and a display panel for
displaying an image in accordance with said drive
waveform, wherein:

said clock generating circuit generates a
clock sequentially switched between at least two
frequencies, and said drive waveform generating circuit
drives said display panel by outputting a drive waveform
whose frequency switches in accordance with said switched
clock.

20. The display apparatus as claimed in claim 19,
wherein said clock generating circuit generates a clock
sequentially switched between two frequencies lying
within plus or minus 1 percent of a reference frequency.

21. The display apparatus as claimed in claim 19,
wherein said display apparatus is a plasma display
apparatus.

22. The display apparatus as claimed in claim 19,
wherein during a quiescent period, said clock generating
circuit performs control of said clock used for driving
said display panel.

23. A display apparatus comprising a clock
generating circuit, a drive waveform generating circuit
for generating a drive waveform by using a clock from
said clock generating circuit, and a display panel for
displaying an image in accordance with said drive
waveform, wherein:

said drive waveform generating circuit
drives said display panel by sequentially switching an

output drive waveform between drive waveforms
corresponding to at least two frequencies.

24. The display apparatus as claimed in claim 23,
wherein said drive waveform generating circuit
5 sequentially switches said output drive waveform between
drive waveforms corresponding to two frequencies lying
within plus or minus 1 percent of a reference frequency.

25. The display apparatus as claimed in claim 23,
wherein said display apparatus is a plasma display
10 apparatus.

26. The display apparatus as claimed in claim 23,
wherein during a quiescent period, said clock generating
circuit performs control of said clock used for driving
said display panel.

ADD
A8

add
B1

add
B2

00750883-0130